
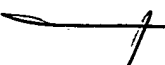




Issue Classification 	Application No.	Applicant(s)	
	10/068,014	JOYNER ET AL.	
	Examiner	Art Unit	
	William M. Brewster	2823	

ISSUE CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
438		184			438	230	744					
INTERNATIONAL CLASSIFICATION												
H	0	1	L	21/338								
H	0	1	L	21/8238								
H	0	1	L	21/302								
H	0	1	L	21/461								
				/								
 23 MAR 04 (Assistant Examiner) (Date)					W. DAVID COLEMAN PRIMARY EXAMINER  (PPrimary Examiner) (Date)					Total Claims Allowed: 14		
 (Legal Instruments Examiner) (Date)										O.G. Print Claim(s) 1	O.G. Print Fig. 2	

<input checked="" type="checkbox"/> Claims renumbered in the same order as presented by applicant												<input type="checkbox"/> CPA		<input type="checkbox"/> T.D.		<input type="checkbox"/> R.1.47	
Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original				
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10/068014
02/05/2002

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10068014	FILING DATE 02/05/2002	CLASS 257 438	SUBCLASS 38 38	GAU 282 2812	EXAMINER Mandala B. S. S.
**APPLICANTS: Joyner Keith; Rodder Mark;					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/266,899 02/06/2001 W/B					
** FOREIGN APPLICATIONS VERIFIED: NONE W/B					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials W/B				ATTORNEY DOCKET NO TI-29912	
TITLE : Method for manufacturing and structure for transistors with reduced gate to contact spacing <small>U.S. DEPT. OF COMM. / PAT. & TM. - PTO-425L (Rev. 12-94)</small>					

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NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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